



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Result of III B.Tech II Semester (R20/R19/R16) Supplementary Examinations, Dec-2023

College name: R.K.COLLEGE OF ENGG.NAVEPOTAVARAM,IBRAHIMPATNAM:MC

Htno	Subcode	Subname	Internals	Grade	Credits
16MC1A0327	R1632031	METROLOGY	19	ABSENT	0
17MC1A0113	R1632011	DESIGN & DRAWING OF STEEL STRUCTURES	25	F	0
17MC1A0422	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	17	F	0
17MC1A0422	R1632043	VLSI DESIGN	18	F	0
17MC1A0430	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	23	F	0
17MC1A0430	R1632043	VLSI DESIGN	25	F	0
17MC1A0430	R1632044	DIGITAL SIGNAL PROCESSING	20	F	0
18MC1A0104	R1632011	DESIGN & DRAWING OF STEEL STRUCTURES	16	ABSENT	0
18MC1A0104	R1632012	GEOTECHNICAL ENGINEERING - I	5	F	0
18MC1A0104	R1632013	ENVIRONMENTAL ENGINEERING -I	5	F	0
18MC1A0104	R1632014	WATER RESOURCE ENGINEERING -I	5	ABSENT	0
18MC1A0104	R163201D	WASTE WATER MANAGEMENT	5	ABSENT	0
18MC1A0311	R1632034	HEAT TRANSFER	16	D	3
18MC1A0406	R1632043	VLSI DESIGN	18	D	3
18MC1A0412	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	17	F	0
18MC1A0412	R1632043	VLSI DESIGN	21	F	0
18MC1A0412	R1632044	DIGITAL SIGNAL PROCESSING	17	F	0
18MC1A0412	R163204A	OOPS THROUGH JAVA	20	F	0
18MC1A0520	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	22	C	3
19MC1A0104	R1932011	DESIGN & DRAWING OF REINFORCED CONCRETE	22	D	3
19MC1A0109	R1932013	GEOTECHNICAL ENGINEERING-I	12	F	0
19MC1A0204	R1932023	DATA STRUCTURES	8	ABSENT	0
19MC1A0338	R1932032	HEAT TRANSFER	16	F	0
19MC1A0402	R1932043	DIGITAL SIGNAL PROCESSING	11	F	0
19MC1A0403	R1932041	WIRED AND WIRELESS TRANSMISSION DEVICES	10	F	0
19MC1A0403	R1932042	VLSI DESIGN	15	F	0
19MC1A0417	R1932042	VLSI DESIGN	17	ABSENT	0
19MC1A0417	R1932043	DIGITAL SIGNAL PROCESSING	15	F	0
19MC1A0417	R193204G	POWER ELECTRONICS	16	ABSENT	0
19MC1A0455	R1932043	DIGITAL SIGNAL PROCESSING	5	F	0
19MC1A0455	R1932044	INTERNET OF THINGS	4	F	0
19MC1A0476	R1932042	VLSI DESIGN	15	D	3
19MC1A0476	R193204A	CELLULAR & MOBILE COMMUNICATION	17	D	3
19MC1A0476	R193204G	POWER ELECTRONICS	12	ABSENT	0
19MC1A0511	R193204J	PRINCIPLES OF COMMUNICATION (EXCEPT ECE)	11	ABSENT	0
19MC1A0511	R193205B	INFORMATION RETRIEVAL SYSTEM	10	F	0
19MC1A0515	R193204J	PRINCIPLES OF COMMUNICATION (EXCEPT ECE)	12	ABSENT	0
19MC1A0515	R1932053	DESIGN AND ANALYSIS OF ALGORITHMS	19	D	3
19MC1A0519	R193204J	PRINCIPLES OF COMMUNICATION (EXCEPT ECE)	6	ABSENT	0
19MC1A0519	R1932051	WEB TECHNOLOGIES	8	F	0
19MC1A0519	R1932052	DISTRIBUTED SYSTEMS	9	F	0
19MC1A0519	R1932053	DESIGN AND ANALYSIS OF ALGORITHMS	7	F	0
19MC1A0519	R1932054	MANAGERIAL ECONOMICS AND FINANCIAL ACCOU	9	F	0
19MC1A0519	R193205B	INFORMATION RETRIEVAL SYSTEM	8	F	0
19MC1A0521	R193204J	PRINCIPLES OF COMMUNICATION (EXCEPT ECE)	8	F	0

Htno	Subcode	Subname	Internals	Grade	Credits
19MC1A0522	R1932051	WEB TECHNOLOGIES	17	C	3
19MC1A0527	R193204J	PRINCIPLES OF COMMUNICATION (EXCEPT ECE)	7	F	0
19MC1A0527	R1932051	WEB TECHNOLOGIES	9	F	0
19MC1A0527	R1932054	MANAGERIAL ECONOMICS AND FINANCIAL ACCOU	5	F	0
19MC1A0549	R193204J	PRINCIPLES OF COMMUNICATION (EXCEPT ECE)	12	ABSENT	0
19MC1A0549	R1932051	WEB TECHNOLOGIES	12	F	0
19MC1A0549	R1932052	DISTRIBUTED SYSTEMS	8	F	0
19MC1A0549	R1932053	DESIGN AND ANALYSIS OF ALGORITHMS	12	F	0
19MC1A0549	R1932054	MANAGERIAL ECONOMICS AND FINANCIAL ACCOU	15	F	0
19MC1A0549	R193205B	INFORMATION RETRIEVAL SYSTEM	9	F	0
19MC1A0551	R1932052	DISTRIBUTED SYSTEMS	10	D	3
19MC1A0551	R1932053	DESIGN AND ANALYSIS OF ALGORITHMS	10	ABSENT	0
19MC1A0551	R193205B	INFORMATION RETRIEVAL SYSTEM	9	ABSENT	0
20MC1A0102	R2032013	GEOTECHNICAL ENGINEERING-II	18	E	3
20MC1A0102	R203201D	TRAFFIC ENGINEERING	21	E	3
20MC1A0103	R2032013	GEOTECHNICAL ENGINEERING-II	19	F	0
20MC1A0103	R203201D	TRAFFIC ENGINEERING	22	E	3
20MC1A0105	R2032013	GEOTECHNICAL ENGINEERING-II	19	F	0
20MC1A0105	R203201D	TRAFFIC ENGINEERING	21	F	0
20MC1A0105	R203201G	DISASTER MANAGEMENT	19	D	3
20MC1A0106	R2032013	GEOTECHNICAL ENGINEERING-II	22	F	0
20MC1A0303	R2032032	DESIGN OF MACHINE MEMBERS-II	22	D	3
20MC1A0308	R2032032	DESIGN OF MACHINE MEMBERS-II	20	F	0
20MC1A0401	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	26	F	0
20MC1A0401	R2032043	DIGITAL SIGNAL PROCESSING	26	D	3
20MC1A0403	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	25	D	3
20MC1A0404	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	14	F	0
20MC1A0404	R2032043	DIGITAL SIGNAL PROCESSING	16	F	0
20MC1A0404	R2032045	VLSI DESIGN LAB	10	A	1.5
20MC1A0404	R2032046	DIGITAL SIGNAL PROCESSING LAB	10	A	1.5
20MC1A0405	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	20	F	0
20MC1A0405	R2032042	VLSI DESIGN	18	E	3
20MC1A0405	R2032043	DIGITAL SIGNAL PROCESSING	17	F	0
20MC1A0405	R203204A	MICROWAVE ENGINEERING	20	D	3
20MC1A0409	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	22	F	0
20MC1A0409	R2032043	DIGITAL SIGNAL PROCESSING	16	F	0
20MC1A0410	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	21	F	0
20MC1A0412	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	18	AB	0
20MC1A0412	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	21	F	0
20MC1A0412	R2032042	VLSI DESIGN	22	AB	0
20MC1A0412	R2032043	DIGITAL SIGNAL PROCESSING	26	F	0
20MC1A0414	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	17	F	0
20MC1A0414	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	20	F	0
20MC1A0414	R2032043	DIGITAL SIGNAL PROCESSING	12	F	0
20MC1A0414	R203204A	MICROWAVE ENGINEERING	21	E	3
20MC1A0415	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	21	D	3
20MC1A0415	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	24	E	3
20MC1A0417	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	21	E	3
20MC1A0417	R2032043	DIGITAL SIGNAL PROCESSING	18	F	0
20MC1A0417	R203204A	MICROWAVE ENGINEERING	22	F	0
20MC1A0419	R2032043	DIGITAL SIGNAL PROCESSING	19	F	0

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20MC1A0420	R2032043	DIGITAL SIGNAL PROCESSING	17	E	3
20MC1A0421	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	25	F	0
20MC1A0421	R2032042	VLSI DESIGN	24	E	3
20MC1A0421	R2032043	DIGITAL SIGNAL PROCESSING	21	E	3
20MC1A0422	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	17	E	3
20MC1A0422	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	9	F	0
20MC1A0422	R2032042	VLSI DESIGN	17	E	3
20MC1A0422	R2032043	DIGITAL SIGNAL PROCESSING	13	F	0
20MC1A0422	R203204A	MICROWAVE ENGINEERING	20	E	3
20MC1A0424	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	27	D	3
20MC1A0424	R2032043	DIGITAL SIGNAL PROCESSING	24	D	3
20MC1A0425	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	17	C	3
20MC1A0425	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	24	D	3
20MC1A0425	R2032042	VLSI DESIGN	22	D	3
20MC1A0425	R2032043	DIGITAL SIGNAL PROCESSING	21	D	3
20MC1A0425	R2032044	MICROPROCESSOR AND MICROCONTROLLERS LAB	10	A	1.5
20MC1A0425	R2032045	VLSI DESIGN LAB	10	A	1.5
20MC1A0425	R2032046	DIGITAL SIGNAL PROCESSING LAB	10	A	1.5
20MC1A0425	R2032047	ARM BASED/AURDINO BASED PROGRAMMING	0	A	2
20MC1A0425	R2032048	RESEARCH METHODOLOGY	27	COMPLE	0
20MC1A0425	R203204A	MICROWAVE ENGINEERING	20	C	3
20MC1A0426	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	17	E	3
20MC1A0426	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	17	F	0
20MC1A0426	R2032042	VLSI DESIGN	13	F	0
20MC1A0426	R2032043	DIGITAL SIGNAL PROCESSING	13	F	0
20MC1A0426	R2032044	MICROPROCESSOR AND MICROCONTROLLERS LAB	10	A	1.5
20MC1A0426	R2032045	VLSI DESIGN LAB	10	A	1.5
20MC1A0426	R2032046	DIGITAL SIGNAL PROCESSING LAB	10	A	1.5
20MC1A0426	R2032047	ARM BASED/AURDINO BASED PROGRAMMING	0	AB	0
20MC1A0426	R2032048	RESEARCH METHODOLOGY	28	COMPLE	0
20MC1A0426	R203204A	MICROWAVE ENGINEERING	18	E	3
20MC1A0428	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	18	F	0
20MC1A0428	R2032043	DIGITAL SIGNAL PROCESSING	12	D	3
20MC1A0430	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	11	AB	0
20MC1A0430	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	17	F	0
20MC1A0430	R2032042	VLSI DESIGN	12	F	0
20MC1A0430	R2032043	DIGITAL SIGNAL PROCESSING	12	F	0
20MC1A0430	R2032044	MICROPROCESSOR AND MICROCONTROLLERS LAB	10	A	1.5
20MC1A0430	R2032045	VLSI DESIGN LAB	10	A	1.5
20MC1A0430	R2032046	DIGITAL SIGNAL PROCESSING LAB	10	A	1.5
20MC1A0430	R2032047	ARM BASED/AURDINO BASED PROGRAMMING	0	A	2
20MC1A0430	R2032048	RESEARCH METHODOLOGY	28	COMPLE	0
20MC1A0430	R203204A	MICROWAVE ENGINEERING	10	F	0
20MC1A0431	R2032043	DIGITAL SIGNAL PROCESSING	20	D	3
20MC1A0432	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	22	E	3
20MC1A0432	R2032043	DIGITAL SIGNAL PROCESSING	12	F	0
20MC1A0432	R203204A	MICROWAVE ENGINEERING	19	C	3
20MC1A0433	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	28	D	3
20MC1A0434	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	21	F	0
20MC1A0434	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	27	F	0
20MC1A0434	R203204A	MICROWAVE ENGINEERING	16	E	3

Htno	Subcode	Subname	Internals	Grade	Credits
20MC1A0437	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	18	E	3
20MC1A0437	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	21	F	0
20MC1A0439	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	28	F	0
20MC1A0439	R2032043	DIGITAL SIGNAL PROCESSING	27	D	3
20MC1A0441	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	24	F	0
20MC1A0441	R2032042	VLSI DESIGN	25	F	0
20MC1A0444	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	22	F	0
20MC1A0444	R2032043	DIGITAL SIGNAL PROCESSING	17	F	0
20MC1A0444	R203204A	MICROWAVE ENGINEERING	12	AB	0
20MC1A0446	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	22	E	3
20MC1A0446	R2032043	DIGITAL SIGNAL PROCESSING	16	D	3
20MC1A0448	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	28	F	0
20MC1A0448	R2032043	DIGITAL SIGNAL PROCESSING	22	F	0
20MC1A0451	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	27	F	0
20MC1A0451	R2032042	VLSI DESIGN	21	F	0
20MC1A0451	R2032043	DIGITAL SIGNAL PROCESSING	23	F	0
20MC1A0452	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	17	F	0
20MC1A0452	R2032043	DIGITAL SIGNAL PROCESSING	16	E	3
20MC1A0452	R203204A	MICROWAVE ENGINEERING	17	E	3
20MC1A0453	R203204A	MICROWAVE ENGINEERING	19	D	3
20MC1A0455	R2032043	DIGITAL SIGNAL PROCESSING	17	C	3
20MC1A0456	R2032043	DIGITAL SIGNAL PROCESSING	16	E	3
20MC1A0465	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	17	F	0
20MC1A0465	R2032042	VLSI DESIGN	17	F	0
20MC1A0465	R2032043	DIGITAL SIGNAL PROCESSING	9	F	0
20MC1A0471	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	19	E	3
20MC1A0471	R2032042	VLSI DESIGN	24	F	0
20MC1A0471	R203204A	MICROWAVE ENGINEERING	20	E	3
20MC1A0472	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	16	F	0
20MC1A0472	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	22	F	0
20MC1A0472	R2032042	VLSI DESIGN	19	F	0
20MC1A0472	R2032043	DIGITAL SIGNAL PROCESSING	16	F	0
20MC1A0472	R203204A	MICROWAVE ENGINEERING	17	F	0
20MC1A0473	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	26	D	3
20MC1A0475	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	24	F	0
20MC1A0479	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	20	F	0
20MC1A0480	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	13	F	0
20MC1A0480	R2032043	DIGITAL SIGNAL PROCESSING	12	AB	0
20MC1A0480	R203204A	MICROWAVE ENGINEERING	15	F	0
20MC1A0481	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	8	F	0
20MC1A0481	R2032042	VLSI DESIGN	13	F	0
20MC1A0481	R2032043	DIGITAL SIGNAL PROCESSING	10	F	0
20MC1A0482	R2032043	DIGITAL SIGNAL PROCESSING	23	D	3
20MC1A0484	R203204A	MICROWAVE ENGINEERING	22	C	3
20MC1A0503	R2032052	COMPILER DESIGN	21	E	3
20MC1A0510	R2032052	COMPILER DESIGN	14	E	3
20MC1A0510	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	18	E	3
20MC1A0512	R203205E	MEAN STACK DEVELOPMENT	23	D	3
20MC1A0516	R2032052	COMPILER DESIGN	15	E	3
20MC1A0516	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	23	D	3
20MC1A0518	R203205E	MEAN STACK DEVELOPMENT	20	E	3

Htno	Subcode	Subname	Internals	Grade	Credits
20MC1A0522	R2032052	COMPILER DESIGN	19	E	3
20MC1A0523	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	20	E	3
20MC1A0523	R2032055	COMPILER DESIGN LAB	9	A	1.5
20MC1A0523	R2032056	CRYPTOGRAPHY AND NETWORK SECURITY LAB	9	A	1.5
20MC1A0523	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	25	D	3
20MC1A0523	R203205E	MEAN STACK DEVELOPMENT	16	F	0
20MC1A0527	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	25	D	3
20MC1A0527	R203205E	MEAN STACK DEVELOPMENT	19	F	0
20MC1A0528	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	28	F	0
20MC1A0531	R203205E	MEAN STACK DEVELOPMENT	22	D	3
20MC1A0534	R2032052	COMPILER DESIGN	12	F	0
20MC1A0536	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	29	D	3
20MC1A0538	R2032052	COMPILER DESIGN	15	E	3
20MC1A0539	R203205E	MEAN STACK DEVELOPMENT	23	E	3
20MC1A0540	R2032052	COMPILER DESIGN	9	E	3
20MC1A0540	R203205E	MEAN STACK DEVELOPMENT	11	E	3
20MC1A0542	R2032051	MACHINE LEARNING	14	F	0
20MC1A0542	R2032052	COMPILER DESIGN	11	F	0
20MC1A0542	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	17	F	0
20MC1A0542	R2032054	MACHINE LEARNING USING PYTHON LAB	10	A	1.5
20MC1A0542	R2032055	COMPILER DESIGN LAB	9	A	1.5
20MC1A0542	R2032056	CRYPTOGRAPHY AND NETWORK SECURITY LAB	9	A	1.5
20MC1A0542	R2032058	SKILL ORIENTED COURSE-IV MEAN STACK TECH	0	A+	2
20MC1A0542	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	14	AB	0
20MC1A0542	R203205E	MEAN STACK DEVELOPMENT	15	F	0
20MC1A0543	R2032055	COMPILER DESIGN LAB	9	A	1.5
20MC1A0543	R2032056	CRYPTOGRAPHY AND NETWORK SECURITY LAB	9	B	1.5
20MC1A0543	R203205E	MEAN STACK DEVELOPMENT	15	AB	0
20MC1A0545	R2032051	MACHINE LEARNING	14	E	3
20MC1A0545	R2032052	COMPILER DESIGN	10	F	0
20MC1A0545	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	23	F	0
20MC1A0545	R203205E	MEAN STACK DEVELOPMENT	15	F	0
20MC1A0548	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	15	E	3
20MC1A0548	R2032055	COMPILER DESIGN LAB	10	A	1.5
20MC1A0548	R203205E	MEAN STACK DEVELOPMENT	10	F	0
20MC1A0557	R2032055	COMPILER DESIGN LAB	9	A	1.5
20MC1A0557	R2032056	CRYPTOGRAPHY AND NETWORK SECURITY LAB	9	A	1.5
20MC1A0557	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	14	E	3
20MC1A0557	R203205E	MEAN STACK DEVELOPMENT	15	F	0
20MC1A0559	R203205E	MEAN STACK DEVELOPMENT	23	E	3
20MC1A0560	R2032054	MACHINE LEARNING USING PYTHON LAB	13	A+	1.5
20MC1A0560	R2032055	COMPILER DESIGN LAB	12	A	1.5
20MC1A0560	R2032056	CRYPTOGRAPHY AND NETWORK SECURITY LAB	12	A+	1.5
20MC1A0560	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	28	F	0
20MC1A0560	R203205E	MEAN STACK DEVELOPMENT	20	F	0
20MC1A0563	R2032052	COMPILER DESIGN	20	E	3
20MC1A0563	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	20	F	0
20MC1A0565	R2032051	MACHINE LEARNING	26	C	3
20MC1A0565	R2032052	COMPILER DESIGN	24	D	3
20MC1A0565	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	25	D	3
20MC1A0565	R2032054	MACHINE LEARNING USING PYTHON LAB	13	A+	1.5

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20MC1A0565	R2032055	COMPILER DESIGN LAB	13	A+	1.5
20MC1A0565	R2032056	CRYPTOGRAPHY AND NETWORK SECURITY LAB	13	A+	1.5
20MC1A0565	R2032058	SKILL ORIENTED COURSE-IV MEAN STACK TECH	0	A+	2
20MC1A0565	R2032059	EMPLOYABILITY SKILLS-II	29	COMPLE	0
20MC1A0565	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	28	C	3
20MC1A0565	R203205E	MEAN STACK DEVELOPMENT	19	C	3
20MC1A0567	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	27	F	0
20MC1A0567	R203205E	MEAN STACK DEVELOPMENT	27	F	0
20MC1A0571	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	29	D	3
20MC1A0579	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	29	D	3
20MC1A0579	R203205E	MEAN STACK DEVELOPMENT	28	D	3
20MC1A0585	R203205E	MEAN STACK DEVELOPMENT	27	B	3
20MC1A0595	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	16	F	0
20MC1A0595	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	23	F	0
20MC1A0595	R203205E	MEAN STACK DEVELOPMENT	15	F	0
20MC1A0596	R2032052	COMPILER DESIGN	15	E	3
20MC5A0208	R1932021	ELECTRIC DRIVES	4	F	0
20MC5A0208	R1932022	POWER SYSTEM ANALYSIS	5	F	0
20MC5A0208	R1932023	DATA STRUCTURES	5	F	0
20MC5A0208	R1932024	DIGITAL CONTROL SYSTEMS	5	F	0
20MC5A0212	R1932023	DATA STRUCTURES	5	ABSENT	0
20MC5A0212	R1932024	DIGITAL CONTROL SYSTEMS	11	ABSENT	0
20MC5A0212	R193204J	PRINCIPLES OF COMMUNICATION (EXCEPT ECE)	14	ABSENT	0
20MC5A0214	R1932023	DATA STRUCTURES	5	F	0
20MC5A0214	R193204J	PRINCIPLES OF COMMUNICATION (EXCEPT ECE)	5	F	0
20MC5A0310	R1932031	OPERATIONS RESEARCH	17	F	0
20MC5A0310	R1932033	CAD/CAM	14	D	3
20MC5A0419	R1932041	WIRED AND WIRELESS TRANSMISSION DEVICES	14	B	3
20MC5A0420	R1932041	WIRED AND WIRELESS TRANSMISSION DEVICES	5	D	3
21A45A0204	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	19	F	0
21A45A0204	R2032022	ELECTRICAL MEASUREMENTS AND INSTRUMENTAT	13	E	3
21A45A0204	R2032023	POWER SYSTEM ANALYSIS	12	E	3
21A45A0204	R203202D	SWITCHGEAR AND PROTECTION	17	E	3
21A45A0204	R203204L	IOT AND APPLICATIONS	16	F	0
21A45A0208	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	20	F	0
21KR5A0209	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	21	E	3
21MC5A0105	R2032011	DESIGN AND DRAWING OF STEEL STRUCTURES	27	F	0
21MC5A0105	R2032013	GEOTECHNICAL ENGINEERING-II	23	F	0
21MC5A0105	R203201G	DISASTER MANAGEMENT	22	D	3
21MC5A0202	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	15	F	0
21MC5A0202	R2032023	POWER SYSTEM ANALYSIS	17	AB	0
21MC5A0202	R203202D	SWITCHGEAR AND PROTECTION	14	F	0
21MC5A0202	R203204L	IOT AND APPLICATIONS	19	E	3
21MC5A0206	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	15	AB	0
21MC5A0206	R2032022	ELECTRICAL MEASUREMENTS AND INSTRUMENTAT	18	AB	0
21MC5A0206	R2032023	POWER SYSTEM ANALYSIS	17	AB	0
21MC5A0206	R203202D	SWITCHGEAR AND PROTECTION	17	AB	0
21MC5A0208	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	22	D	3
21MC5A0215	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	20	D	3
21MC5A0215	R2032022	ELECTRICAL MEASUREMENTS AND INSTRUMENTAT	21	D	3
21MC5A0220	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	22	D	3

Htno	Subcode	Subname	Internals	Grade	Credits
21MC5A0220	R2032022	ELECTRICAL MEASUREMENTS AND INSTRUMENTAT	18	D	3
21MC5A0222	R2032022	ELECTRICAL MEASUREMENTS AND INSTRUMENTAT	17	E	3
21MC5A0224	R203202D	SWITCHGEAR AND PROTECTION	26	AB	0
21MC5A0303	R2032031	HEAT TRANSFER	22	F	0
21MC5A0303	R2032032	DESIGN OF MACHINE MEMBERS-II	25	F	0
21MC5A0303	R203203H	ESSENTIALS OF MECHANICAL ENGINEERING	21	E	3
21MC5A0304	R203203A	AUTOMOBILE ENGINEERING	20	E	3
21MC5A0304	R203203H	ESSENTIALS OF MECHANICAL ENGINEERING	19	AB	0
21MC5A0307	R2032032	DESIGN OF MACHINE MEMBERS-II	23	E	3
21MC5A0310	R2032033	INTRODUCTION TO ARTIFICIAL INTELLIGENCE	21	F	0
21MC5A0313	R2032032	DESIGN OF MACHINE MEMBERS-II	19	F	0
21MC5A0313	R2032033	INTRODUCTION TO ARTIFICIAL INTELLIGENCE	20	F	0
21MC5A0315	R203203A	AUTOMOBILE ENGINEERING	25	D	3
21MC5A0318	R2032031	HEAT TRANSFER	23	F	0
21MC5A0318	R203203H	ESSENTIALS OF MECHANICAL ENGINEERING	19	E	3
21MC5A0321	R2032032	DESIGN OF MACHINE MEMBERS-II	17	F	0
21MC5A0321	R203203H	ESSENTIALS OF MECHANICAL ENGINEERING	17	AB	0
21MC5A0322	R2032031	HEAT TRANSFER	17	F	0
21MC5A0322	R2032032	DESIGN OF MACHINE MEMBERS-II	18	F	0
21MC5A0322	R2032033	INTRODUCTION TO ARTIFICIAL INTELLIGENCE	20	F	0
21MC5A0322	R203203A	AUTOMOBILE ENGINEERING	20	AB	0
21MC5A0322	R203203H	ESSENTIALS OF MECHANICAL ENGINEERING	19	AB	0
21MC5A0325	R2032033	INTRODUCTION TO ARTIFICIAL INTELLIGENCE	19	E	3
21MC5A0325	R203203H	ESSENTIALS OF MECHANICAL ENGINEERING	17	E	3
21MC5A0335	R2032036	MEASUREMENTS & METROLOGY LAB	14	A	1.5
21MC5A0336	R203203H	ESSENTIALS OF MECHANICAL ENGINEERING	24	F	0
21MC5A0403	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	22	F	0
21MC5A0403	R2032043	DIGITAL SIGNAL PROCESSING	16	F	0
21MC5A0403	R203204A	MICROWAVE ENGINEERING	17	D	3
21MC5A0413	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	24	D	3
21MC5A0413	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	24	F	0
21MC5A0413	R2032043	DIGITAL SIGNAL PROCESSING	16	E	3
21MC5A0414	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	17	E	3
21MC5A0414	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	19	F	0
21MC5A0414	R2032042	VLSI DESIGN	23	D	3
21MC5A0414	R2032043	DIGITAL SIGNAL PROCESSING	18	E	3
21MC5A0414	R2032044	MICROPROCESSOR AND MICROCONTROLLERS LAB	9	B	1.5
21MC5A0414	R2032045	VLSI DESIGN LAB	10	A	1.5
21MC5A0414	R2032046	DIGITAL SIGNAL PROCESSING LAB	10	A	1.5
21MC5A0414	R2032047	ARM BASED/AURDINO BASED PROGRAMMING	0	A	2
21MC5A0414	R203204A	MICROWAVE ENGINEERING	16	D	3
21MC5A0427	R203202G	FUNDAMENTALS OF UTILIZATION OF ELECTRICA	17	F	0
21MC5A0427	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	16	F	0
21MC5A0427	R2032042	VLSI DESIGN	17	F	0
21MC5A0501	R2032051	MACHINE LEARNING	17	E	3
21MC5A0501	R2032052	COMPILER DESIGN	17	F	0
21MC5A0501	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	14	E	3
21MC5A0501	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	22	D	3
21MC5A0503	R203205E	MEAN STACK DEVELOPMENT	26	D	3
21MC5A0504	R2032052	COMPILER DESIGN	16	E	3
21MC5A0504	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	20	F	0

Htno	Subcode	Subname	Internals	Grade	Credits
21MC5A0504	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	23	F	0
21MC5A0504	R203205E	MEAN STACK DEVELOPMENT	25	F	0
21MC5A0505	R2032051	MACHINE LEARNING	25	F	0
21MC5A0505	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	19	F	0
21MC5A0505	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	20	F	0
21MC5A0505	R203205E	MEAN STACK DEVELOPMENT	26	D	3
21MC5A0507	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	20	D	3
21MC5A0507	R203205E	MEAN STACK DEVELOPMENT	22	D	3
21MC5A0510	R2032051	MACHINE LEARNING	24	F	0
21MC5A0510	R2032052	COMPILER DESIGN	16	E	3
21MC5A0510	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	22	D	3
21MC5A0510	R2032054	MACHINE LEARNING USING PYTHON LAB	12	A+	1.5
21MC5A0510	R2032055	COMPILER DESIGN LAB	12	A	1.5
21MC5A0510	R2032056	CRYPTOGRAPHY AND NETWORK SECURITY LAB	12	A+	1.5
21MC5A0510	R2032058	SKILL ORIENTED COURSE-IV MEAN STACK TECH	0	A+	2
21MC5A0510	R2032059	EMPLOYABILITY SKILLS-II	28	COMPLE	0
21MC5A0510	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	24	AB	0
21MC5A0510	R203205E	MEAN STACK DEVELOPMENT	23	AB	0
21MC5A0514	R2032051	MACHINE LEARNING	14	F	0
21MC5A0514	R2032052	COMPILER DESIGN	20	E	3
21MC5A0514	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	15	F	0
21MC5A0514	R2032054	MACHINE LEARNING USING PYTHON LAB	11	A+	1.5
21MC5A0514	R2032055	COMPILER DESIGN LAB	12	A	1.5
21MC5A0514	R2032056	CRYPTOGRAPHY AND NETWORK SECURITY LAB	12	A	1.5
21MC5A0514	R2032058	SKILL ORIENTED COURSE-IV MEAN STACK TECH	0	A+	2
21MC5A0514	R2032059	EMPLOYABILITY SKILLS-II	27	COMPLE	0
21MC5A0514	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	25	E	3
21MC5A0514	R203205E	MEAN STACK DEVELOPMENT	26	D	3
21MC5A0515	R2032052	COMPILER DESIGN	21	E	3
21MC5A0516	R2032052	COMPILER DESIGN	24	F	0
21MC5A0516	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	21	F	0
21MC5A0516	R203205E	MEAN STACK DEVELOPMENT	25	F	0
21MC5A0517	R2032052	COMPILER DESIGN	16	F	0
21MC5A0517	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	22	F	0
21MC5A0517	R203205E	MEAN STACK DEVELOPMENT	25	F	0

**Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 11-03-2024]

** Note:**

* -1 in the filed of externals indicates student is absent for the respective subject.

* -2 in the filed of externals or (WH) in grade indicates student result Withheld for the respective subject.

* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.

H. Reberic

Date:01.03.2024

Controller of Examinations(UG)