

Result of M.Tech I SEMESTER (R20/R19) REG/SUPPLE. EXAMINATIONS, March-2023 College name: R.K.COLLEGE OF ENGG.NAVEPOTAVARAM,IBRAHIMPATNAM:MC

College Harrie. N.N.COLLEGE OF ENGO.NAVELOTAVAKANI, IDIKA HIVII A TNAMI.NIC

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Htno	Subcode	Subname	Internals	Grade	Credits
19MC1D6801	M5505	VLSI SIGNAL PROCESSING (ELECTIVE-I)	23	F	0
19MC1D6806	M5505	VLSI SIGNAL PROCESSING (ELECTIVE-I)	23	F	0
19MC1D6806	M6801	RTL SIMULATION AND SYNTHESIS WITH PLDS	23	F	0
20MC1D1502	M1501	ADVANCED MECHANICS OF SOLIDS	21	С	3
20MC1D1505	M1501	ADVANCED MECHANICS OF SOLIDS	21	С	3
20MC1D5202	M4303	MODERN CONTROL THEORY (ELECTIVE-I)	20	F	0
20MC1D5205	M0109	RESEARCH METHODOLOGY AND IPR	16	F	0
20MC1D5205	M4303	MODERN CONTROL THEORY (ELECTIVE-I)	20	F	0
20MC1D5206	M0109	RESEARCH METHODOLOGY AND IPR	16	F	0
20MC1D5206	M4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	19	F	0
20MC1D5206	M4303	MODERN CONTROL THEORY (ELECTIVE-I)	19	F	0
20MC1D5206	M4307	HVDC TRANSMISSION AND FLEXIBLE AC TRANSM	21	F	0
20MC1D5807	M5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	14	F	0
20MC1D5810	M5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	23	F	0
20MC1D5810	M5802	ADVANCED DATA STRUCTURES & ALGORITHMS	20	F	0
20MC1D5810	M5806	ADVANCED COMPUTER NETWORKS (ELECTIVE-II)	20	F	0
20MC1D6801	M5502	MICROCONTROLLERS AND PROGRAMMABLE DIGITA	23	F	0
20MC1D6802	M5502	MICROCONTROLLERS AND PROGRAMMABLE DIGITA	23	F	0
20MC1D6802	M5505	VLSI SIGNAL PROCESSING (ELECTIVE-I)	23	F	0
20MC1D6802	M5506	PROGRAMMING LANGUAGES FOR EMBEDDED SYSTE	21	F	0
20MC1D6802	M6801	RTL SIMULATION AND SYNTHESIS WITH PLDS	24	F	0
20MC1D6804	M5502	MICROCONTROLLERS AND PROGRAMMABLE DIGITA	24	F	0
20MC1D6808	M5502	MICROCONTROLLERS AND PROGRAMMABLE DIGITA	25	F	0
20MC1D6813	M5502	MICROCONTROLLERS AND PROGRAMMABLE DIGITA	25	F	0
20MC1D6814	M5502	MICROCONTROLLERS AND PROGRAMMABLE DIGITA	24	F	0
20MC1D6814	M6801	RTL SIMULATION AND SYNTHESIS WITH PLDS	24	F	0
20MC1D6815	M5505	VLSI SIGNAL PROCESSING (ELECTIVE-I)	25	AB	0
20MC1D6815	M5506	PROGRAMMING LANGUAGES FOR EMBEDDED SYSTE	23	AB	0
21MC1D1502	M1501	ADVANCED MECHANICS OF SOLIDS	23	В	3
21MC1D5202	M4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	24	AB	0
21MC1D5202	M4303	MODERN CONTROL THEORY (ELECTIVE-I)	23	AB	0
21MC1D5205	M4301	ELECTRICAL MACHINES MODELING AND ANALYSI	23	AB	0
21MC1D5205	M4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	24	AB	0
21MC1D5801	M0109	RESEARCH METHODOLOGY AND IPR	22	F	0
21MC1D5801	M5806	ADVANCED COMPUTER NETWORKS (ELECTIVE-II)	22	F	0
21MC1D5803	M5805	ADVANCED OPERATING SYSTEMS (ELECTIVE-I)	23	С	3
21MC1D5809	M0109	RESEARCH METHODOLOGY AND IPR	19	F	0
21MC1D5809	M5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	20	AB	0
21MC1D5809	M5802	ADVANCED DATA STRUCTURES & ALGORITHMS	19	F	0
21MC1D5809	M5805	ADVANCED OPERATING SYSTEMS (ELECTIVE-I)	18	AB	0
21MC1D5809	M5806	ADVANCED COMPUTER NETWORKS (ELECTIVE-II)	19	F	0
21MC1D5809	M5809	ADVANCED DATA STRUCTURES & ALGORITHMS LA	18	S	2
21MC1D5809	M5810	ADVANCED COMPUTING LAB	19	S	2
21MC1D5809	MAC05	CONSTITUTION OF INDIA	0	COMPLE	0
21MC1D6802	M5505	VLSI SIGNAL PROCESSING (ELECTIVE-I)	24	F	0

21MC1D6803	Credits
22MC1D1501 M0109 RESEARCH METHODOLOGY AND IPR 23 F	2
22MC1D1501 M1303 DESIGN FOR MANUFACTURING & ASSEMBLY (ELE 23 C 22MC1D1501 M1501 ADVANCED MECHANICS OF SOLIDS 23 F 22MC1D1501 M1504 MECHANICAL VIBRATIONS AND ACOUSTICS 23 F 22MC1D1501 M1504 PRODUCT DESIGN (ELECTIVE-I) 24 F 22MC1D1501 M1513 DESIGN PRACTICE LAB-I 24 O 22MC1D1501 M1513 DESIGN PRACTICE LAB-I 24 O 22MC1D5202 M0109 RESEARCH METHODOLOGY AND IPR 24 AB 22MC1D5202 M4301 ELECTRICAL MACHINES MODELING AND ANALYSI 24 AB 22MC1D5202 M4303 ELECTRICAL MACHINES MODELING AND ANALYSI 24 AB 22MC1D5202 M4303 MODERN CONTROL THEORY (ELECTIVE-I) 24 AB 22MC1D5202 M4303 HVDC TRANSMISSION AND FLEXIBLE AC TRANSM 23 AB 22MC1D5202 M4308 POWER ELECTRONICS SIMULATION LABORATORY 24 S 22MC1D58001 M3809 POWER CONVERTERS LABORATORY 24 </td <td>0</td>	0
22MC1D1501 M1501 ADVANCED MECHANICS OF SOLIDS 23 F 22MC1D1501 M1502 MECHANICAL VIBRATIONS AND ACOUSTICS 23 F 22MC1D1501 M1502 MECHANICAL VIBRATIONS AND ACOUSTICS 23 F 22MC1D1501 M1512 MACHINE DYNAMICS LAB 23 S 22MC1D1501 M1513 DESIGN PRACTICE LAB-I 24 O 22MC1D5202 M4301 DESIGN PRACTICE LAB-I Q COMPLE 22MC1D5202 M4301 RESEARCH METHODOLOGY AND IPR 24 AB 22MC1D5202 M4302 ANALYSIS OF POWER ELECTRONIC CONVERTERS 24 AB 22MC1D5202 M4303 MODERN CONTROL THEORY (ELECTIVE-I) 24 AB 22MC1D5202 M4303 HVDC TRANSMISSION AND FLEXIBLE AC TRANSM 23 AB 22MC1D5202 M4308 POWER ELECTRONICS SIMULATION LABORATORY 23 O 22MC1D5801 M4308 POWER CONVERTERS LABORATORY 24 S 22MC1D5801 M5801 MATHEMATICAL FOUNDATIONS OF COMPUTER SCI 22	0
22MC1D1501 M1502 MECHANICAL VIBRATIONS AND ACOUSTICS 23 F 22MC1D1501 M1504 PRODUCT DESIGN (ELECTIVE-I) 24 F 22MC1D1501 M1504 PRODUCT DESIGN (ELECTIVE-I) 24 F 22MC1D1501 M1513 DESIGN PRACTICE LAB-I 24 O COMPLE 22MC1D5202 M4019 RESEARCH METHODOLOGY AND IPR 24 AB I 22MC1D5202 M4301 ELECTRICAL MACHINES MODELING AND ANALYSI 24 AB I 22MC1D5202 M4303 ANALYSIS OF POWER ELECTRONIC CONVERTERS 24 AB I 22MC1D5202 M4303 MODERN CONTROL THEORY (ELECTIVE-I) 24 AB I 22MC1D5202 M4303 HVDC TRANSMISSION AND FLEXIBLE AC TRANSM 23 AB I 22MC1D5202 M4308 POWER ELECTRONICS SIMULATION LABORATORY 23 O COMPLE 22MC1D5801 M4309 POWER CONVERTERS LABORATORY 24 S 3 22MC1D5801 M5801 MACO2 DISASTER MANAGEMENT	3
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22MC1D5202 M4302 ANALYSIS OF POWER ELECTRONIC CONVERTERS 24 AB 22MC1D5202 M4303 MODERN CONTROL THEORY (ELECTIVE-I) 24 AB 22MC1D5202 M4307 HVDC TRANSMISSION AND FLEXIBLE AC TRANSM 23 AB 22MC1D5202 M4308 POWER ELECTRONICS SIMULATION LABORATORY 23 O 22MC1D5202 M4309 POWER CONVERTERS LABORATORY 24 S 22MC1D5202 MAC02 DISASTER MANAGEMENT 0 COMPLE 22MC1D5801 M0109 RESEARCH METHODOLOGY AND IPR 21 C 22MC1D5801 M5801 MATHEMATICAL FOUNDATIONS OF COMPUTER SCI 22 F 22MC1D5801 M5802 ADVANCED DATA STRUCTURES & ALGORITHMS 22 C 3 22MC1D5801 M5806 ADVANCED OPERATING SYSTEMS (ELECTIVE-I) 21 B 3 22MC1D5801 M5806 ADVANCED COMPUTER NETWORKS (ELECTIVE-II) 21 B 3 22MC1D5801 M5806 ADVANCED DATA STRUCTURES & ALGORITHMS LA 22 O 3	0
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22MC1D5802 MAC05 CONSTITUTION OF INDIA 0 COMPLE 22MC1D5803 M0109 RESEARCH METHODOLOGY AND IPR 18 F 22MC1D5803 M5801 MATHEMATICAL FOUNDATIONS OF COMPUTER SCI 18 F 22MC1D5803 M5802 ADVANCED DATA STRUCTURES & ALGORITHMS 18 F	2
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22MC1D5803 M5806 ADVANCED COMPUTER NETWORKS (ELECTIVE-II) 17 F	0
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Htno	Subcode	Subname	Internals	Grade	Credits
22MC1D5805	M5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	22	F	0
22MC1D5805	M5802	ADVANCED DATA STRUCTURES & ALGORITHMS	22	С	3
22MC1D5805	M5805	ADVANCED OPERATING SYSTEMS (ELECTIVE-I)	21	С	3
22MC1D5805	M5806	ADVANCED COMPUTER NETWORKS (ELECTIVE-II)	22	С	3
22MC1D5805	M5809	ADVANCED DATA STRUCTURES & ALGORITHMS LA	21	S	2
22MC1D5805	M5810	ADVANCED COMPUTING LAB	22	0	2
22MC1D5805	MAC05	CONSTITUTION OF INDIA	0	COMPLE	0
22MC1D5806	M0109	RESEARCH METHODOLOGY AND IPR	22	F	0
22MC1D5806	M5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	21	F	0
22MC1D5806	M5802	ADVANCED DATA STRUCTURES & ALGORITHMS	20	F	0
22MC1D5806	M5805	ADVANCED OPERATING SYSTEMS (ELECTIVE-I)	22	F	0
22MC1D5806	M5806	ADVANCED COMPUTER NETWORKS (ELECTIVE-II)	20	F	0
22MC1D5806	M5809	ADVANCED DATA STRUCTURES & ALGORITHMS LA	16	S	2
22MC1D5806	M5810	ADVANCED COMPUTING LAB	18	S	2
22MC1D5806	MAC05	CONSTITUTION OF INDIA	0	COMPLE	0
22MC1D5807	M0109	RESEARCH METHODOLOGY AND IPR	20	С	2
22MC1D5807	M5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	21	F	0
22MC1D5807	M5802	ADVANCED DATA STRUCTURES & ALGORITHMS	21	С	3
22MC1D5807	M5805	ADVANCED OPERATING SYSTEMS (ELECTIVE-I)	20	С	3
22MC1D5807	M5806	ADVANCED COMPUTER NETWORKS (ELECTIVE-II)	20	С	3
22MC1D5807	M5809	ADVANCED DATA STRUCTURES & ALGORITHMS LA	20	S	2
22MC1D5807	M5810	ADVANCED COMPUTING LAB	22	0	2
22MC1D5807	MAC05	CONSTITUTION OF INDIA	0	COMPLE	0
22MC1D5808	M0109	RESEARCH METHODOLOGY AND IPR	22	С	2
22MC1D5808	M5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	21	F	0
22MC1D5808	M5802	ADVANCED DATA STRUCTURES & ALGORITHMS	21	С	3
22MC1D5808	M5805	ADVANCED OPERATING SYSTEMS (ELECTIVE-I)	21	С	3
22MC1D5808	M5806	ADVANCED COMPUTER NETWORKS (ELECTIVE-II)	22	С	3
22MC1D5808	M5809	ADVANCED DATA STRUCTURES & ALGORITHMS LA	21	0	2
22MC1D5808	M5810	ADVANCED COMPUTING LAB	22	S	2
22MC1D5808	MAC05	CONSTITUTION OF INDIA	0	COMPLE	0
22MC1D5809	M0109	RESEARCH METHODOLOGY AND IPR	22	F	0
22MC1D5809	M5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	22	F	0
22MC1D5809	M5802	ADVANCED DATA STRUCTURES & ALGORITHMS	22	F	0
22MC1D5809	M5805	ADVANCED OPERATING SYSTEMS (ELECTIVE-I)	22	F	0
22MC1D5809	M5806	ADVANCED COMPUTER NETWORKS (ELECTIVE-II)	22	F	0
22MC1D5809	M5809	ADVANCED DATA STRUCTURES & ALGORITHMS LA	22	0	2
22MC1D5809	M5810	ADVANCED COMPUTING LAB	23	0	2
22MC1D5809	MAC05	CONSTITUTION OF INDIA	0	COMPLE	0
22MC1D5810	M0109	RESEARCH METHODOLOGY AND IPR	18	AB	0
22MC1D5810	M5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	18	AB	0
22MC1D5810	M5802	ADVANCED DATA STRUCTURES & ALGORITHMS	18	F	0
22MC1D5810	M5805	ADVANCED OPERATING SYSTEMS (ELECTIVE-I)	18	AB	0
22MC1D5810	M5806	ADVANCED COMPUTER NETWORKS (ELECTIVE-II)	17	F	0
22MC1D5810	M5809	ADVANCED DATA STRUCTURES & ALGORITHMS LA	20	AB	0
22MC1D5810	M5810	ADVANCED COMPUTING LAB	22	AB	0
22MC1D5810	MAC05	CONSTITUTION OF INDIA	0	COMPLE	0
22MC1D5811	M0109	RESEARCH METHODOLOGY AND IPR	22	F	0
22MC1D5811	M5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	21	F	0
	M5802	ADVANCED DATA STRUCTURES & ALGORITHMS	21	F	0
22MC1D5811	IVIOUCE				

Htno	Subcode	Subname	Internals	Grade	Credits
22MC1D5811	M5806	ADVANCED COMPUTER NETWORKS (ELECTIVE-II)	22	F	0
22MC1D5811	M5809	ADVANCED DATA STRUCTURES & ALGORITHMS LA	21	0	2
22MC1D5811	M5810	ADVANCED COMPUTING LAB	22	0	2
22MC1D5811	MAC05	CONSTITUTION OF INDIA	0	COMPLE	0
22MC1D5812	M0109	RESEARCH METHODOLOGY AND IPR	22	С	2
22MC1D5812	M5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	22	F	0
22MC1D5812	M5802	ADVANCED DATA STRUCTURES & ALGORITHMS	22	С	3
22MC1D5812	M5805	ADVANCED OPERATING SYSTEMS (ELECTIVE-I)	22	С	3
22MC1D5812	M5806	ADVANCED COMPUTER NETWORKS (ELECTIVE-II)	22	F	0
22MC1D5812	M5809	ADVANCED DATA STRUCTURES & ALGORITHMS LA	22	0	2
22MC1D5812	M5810	ADVANCED COMPUTING LAB	23	0	2
22MC1D5812	MAC05	CONSTITUTION OF INDIA	0	COMPLE	0
22MC1D6802	M0109	RESEARCH METHODOLOGY AND IPR	23	С	2
22MC1D6802	M5502	MICROCONTROLLERS AND PROGRAMMABLE DIGITA	23	С	3
22MC1D6802	M5505	VLSI SIGNAL PROCESSING (ELECTIVE-I)	22	F	0
22MC1D6802	M5506	PROGRAMMING LANGUAGES FOR EMBEDDED SYSTE	23	F	0
22MC1D6802	M6801	RTL SIMULATION AND SYNTHESIS WITH PLDS	23	F	0
22MC1D6802	M6802	RTL SIMULATION AND SYNTHESIS WITH PLDS L	22	S	2
22MC1D6802	M6803	MICROCONTROLLERS AND PROGRAMMABLE DIGITA	22	0	2
22MC1D6802	MAC01	ENGLISH FOR RESEARCH PAPER WRITING	0	COMPLE	0
22MC1D6803	M0109	RESEARCH METHODOLOGY AND IPR	23	F	0
22MC1D6803	M5502	MICROCONTROLLERS AND PROGRAMMABLE DIGITA	23	F	0
22MC1D6803	M5505	VLSI SIGNAL PROCESSING (ELECTIVE-I)	23	F	0
22MC1D6803	M5506	PROGRAMMING LANGUAGES FOR EMBEDDED SYSTE	23	С	3
22MC1D6803	M6801	RTL SIMULATION AND SYNTHESIS WITH PLDS	23	F	0
22MC1D6803	M6802	RTL SIMULATION AND SYNTHESIS WITH PLDS L	23	S	2
22MC1D6803	M6803	MICROCONTROLLERS AND PROGRAMMABLE DIGITA	24	0	2
22MC1D6803	MAC01	ENGLISH FOR RESEARCH PAPER WRITING	0	COMPLE	0

^{**}Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is: 04-06-2023]

Date:24.05.2023 Controller of Examinations(PG)

^{**} Note:**

^{* -1} in the filed of externals indicates student is absent for the respective subject.

^{* -2} in the filed of externals or (WH) in grade indicates student result Withheld for the respective subject.

^{* -3} in the filed of externals indicates student involved in Malpractice for the respective subject.