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Kethanakonda (V), Ibrahimpatnam (M), Vijayawada, AMARAVATI - AP - 521456





DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

COURSE OUTCOMES (COs)

Course Outcomes (COs) describe what students can able to do after completion of the course.

S.No	Year- Sem	Course Code	Course Name	Course Outcomes After completion of the course student can able to
1	11-1	PCC	Electronic Devices And	CO1: Understand the principles of semiconductor Physics. CO2: Analyze and create application of special purpose diodes, and electronic circuits CO3:Understand carrier transport in semiconductors.
•		700	Circuits	CO4:Analyze and evaluate the mathematical models of MOS&BJT transistors for circuits and systems. CO5:Analyze and create application of transistor amplifier models
				CO1:Interpret the physical meaning of different operators such as gradient, cur land divergence (L5) CO2:Estimate the work done against a field, circulation and flux using vector calculus (L5) CO3:Apply the Laplace transform for solving differential equations (L3) CO4:Find or compute the Fourier series of periodic
2	11-1	BSC	M-III	cO5:Know and be able to apply integral expressions for the forwards and inverse Fourier transform to arrange of non-periodic wave forms (L3) Identify solution methods for partial differential equations that model physical process
				CO1:Differentiate the various classifications of signals and systems CO2:Analyze the frequency domain
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				Swings and AWT.
7	11-1	PCC Lab	Electronic Devices And Circuits Lab	CO 1: Measure Voltage, frequency and phase of any wave form using CRO CO 2: Generate sine, square and triangular waveforms with required frequency and amplitude using function generator CO 3: Analyze the characteristics of different electronic devices such as diodes, transistors etc. and simple circuits like rectifiers, amplifiers etc.,
8	11-1	PCC Lab	Switching Theory And Logic Design Lab	CO 1: Students Able to design simple combinational circuit by using basic logic gates. CO 2: Students are able to design full adder circuit and verify its functional table. CO 3: Students are able to verify functional tables of (i.) JK Edge Triggered Flip- Flop (ii.) JK Master Slave Flip Flop CO 4: Students are able to design a four bit ring counter using D- Flip Flops/JK Flip-Flops. CO 5: Able to design a four bit Johnson's Counter using D-Flip Flops/JK Flip-Flops.
9	11-11	PCC	Digital IC Design	CO1: Understand the structure of commercially available digital integrated circuit families. CO2: Learn the IEEE Standard 1076 Hardware Description Language (VHDL). CO3: Model complex digital systems at several levels of abstractions, behavioral, structural, and rapid system prototyping. CO4: Analyze and design basic digital circuits with combinatorial circuits using VHDL. CO5: Analyze and design basic digital circuits with sequential logic circuits using VHDL.
			SE OF EN	CO1: Design and analysis of small signal high frequency transistor amplifier using BJT and FET. CO2: Design and analysis of multi stage amplifiers using BJT and FET and Differential amplifier using BJT CO3: Derive the expressions for frequency of oscillation and condition for oscillation of RC and LC oscillators and their amplitude and frequency stability concept.



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10	11-11	BSC/PC	Electronic	CO4: Know the classification of the power and
			Circuit Analysis	tuned amplifiers and their analysis with
				performance comparison
				CO5: Know the classification of the feedback
				amplifiers and their analysis with performance
				comparison
				CO1: Understand the basic concepts of the analog
				communication systems
				CO2: Evaluate modulation index, bandwidth and
				power requirements for various analog
				modulation schemes including AM,FM and PM
				CO3: Understand and Analyze various analog
				continuous wave modulation and demodulation
			Analog	techniques including AM, FM and PM
11	11-11	PCC	Communication	CO4: Analyze various analog pulse modulation
		- 14 m	S	and demodulation techniques including AM, FM
				and PM
				CO5: Understand the influence of noise over
				Analog Modulation schemes through random
				process and noise theory and applications of
				Analog communication techniques.
				CO1:This course introduces the concepts of
				feedback and its advantages to various control
				systems
				CO2: The performance metrics to design the
			10	control system in time-domain and frequency
12	11-11	ESC	Linear control	domain are introduced
	11-11		Systems	CO3: Control systems for various applications can
				be designed using time-domain and frequency
				domain analysis.
				CO4: In addition to the conventional approach,
				the state space approach for the analysis of
				control systems is also introduced.
				CO1: After completion of the Course the student
				will acquire the knowledge on management
				functions, global leadership and organizational
				structure.
				CO2: Will familiarize with the concepts of functional management that is HRM and
			A STATE OF THE PARTY OF THE PAR	o interi aria
			COE VI EN	Marketing of new product developments.

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10	11 11			am (M), Vijayawada, AMARAVATI - AP - 521456
13	11-11	HSS	Management	CO3: The learner is able to think in strategically
			And	through contemporary management practices.
			Organizational	CO4: The learner can develop positive attitude
			Behaviour	through personality development and can equip
				with motivational theories.
				CO5: The student can attain the group
				performance and grievance handling in managing
				the organizational culture.
				CO 1: Comprehend the fundamentals of multi
				stage amplifiers, feedback power amplifiers and
				oscillator circuits.
				CO 2: Analyze the circuit design process and
				simulate the common base, common emitter and
				common collector amplifier circuits.
				CO 3: Acquaint with the design and simulate the
				RC coupled and Cascade amplifier circuits
14	11-11	PCC Lab	Electronic	CO 4: Discriminate the design and simulate the
			Circuit Analysis	RC coupled and Cascade amplifier circuits
			Lab	CO 5: Interpret to design and simulate various
				oscillator circuits
	0.0			CO 6: Create the design and simulate the cascade,
				class A power amplifier circuits, and single tuned
				voltage amplifier circuits
				CO 1: Analyze the concepts, write and simulate
				the concepts of AM and AM Demodulation
				process in Communication
				CO 2: Know the origin and simulation of FM and
				FM- Demodulation process in communication
			Analog	CO 3: Acquaint with AM and FM basic
15	11-11	PCC Lab	Communication	functionalities
			s Lab	CO 4: Discriminate the AM and FM functionalities
				CO 5: Interpret with various angle modulation
				and demodulation systems
				CO 6: Create the writing and simulation
				environments in PWM, PPM, Mixer and ring
				modulation
				CO1: Students should enable to describe and
				explain the operation of fundamental digital
			EOF	gates
		1/30	The state of the s	

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16	11-11	PCC Lab	Digital IC	CO2: Get the knowledge and use of hardware descriptive language(VHDL) for system modeling
			Design Lab	and simulation.
				CO3: Students Can Analyze the operation of
				medium complexity standard combinational
				circuits like encoder, decoder, multiplexer,
				multiplexers and develop corresponding VHDL
				code in one of the model.
				CO4: Students can design complex digital
				systems at several level of abstractions,
				behavioral and structural, synthesis and rapid
				system prototyping
				CO5: Students can design complex digital system
				such as ALU.
				CO1:Design circuits using operational amplifiers
				for various applications CO2: Analyze and design amplifiers and active
				filters using Op-amp.
			Linear IC	CO3: Diagnose and trouble-shoot linear
17	111-1	PC	Applications	electronic circuits.
				CO4: Understand the gain-bandwidth concept
				and frequency response of the amplifier configurations
				CO5: Understand thoroughly the operational
				amplifiers with linear integrated circuits.
				CO1: Understand about basic Computing
				Architectures, 8086 – Micro Processor Pin
				Diagram, Architecture, Interrupt Structure and
				Timing Diagrams in Maximum and Minimum mode.
10		DC	Micro	CO2: Understand about Instruction Set and
18	111-1	PC	Processors and Micro	various tools for developing 8086 Assembly
			Controllers	Language Program.
			20 011613	CO3: Acquire the knowledge on interfacing
				various peripherals, configure and develop
			L divers	programs to interface peripherals. CO4: 8051 Micro Controller Pin Diagram
		-	EOF	CO4: 8051 Micro Controller Pin Diagram,



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				Architecture and able to write 8051 Assembly Language Program for interfacing concepts.
				CO5: Able to develop programs efficiently on
				ARM Cortex Processors and debug.
				CO1: Design a coded communication system.
10			Digital	CO2: Analyze the performance of a Digital Communication System for probability of error and are able to design a digital communication system.
19	111-1	PC	Communication	/ techniques.
			S	CO4: Analyze various source coding techniques.
				CO5: Compute and analyze Block codes, cyclic codes and convolution codes
				CO1: Select the instrument to be used based on the requirements CO2: Understand and analyze different signal
				generators and analyzers.
20	111-1	PC	Electronic	CO3: Understand the different types of Oscilloscopes for different applications
			Measurement and	CO4: Understand and analyze the concepts Bridge circuits in measuring equipment
			Instrumentation	CO5: Design different transducers for measurement of different parameters.
				CO1: Understand the architecture of FPGAs, tools used in modeling of digital design
			District Con	CO2: Understanding and practice the operators and data types and different modiling concepts of design of hardware circuits in very log.
21	111-1	I-I PE	Digital System Design Using HDL	CO3: Analyze and design basic digital circuits with
21	111-1			combinatorial logic circuits using Verilog HDL.
				CO4: Analyze and design basic digital circuits with sequential logic circuits using Verilog HDL.
				CO5: Design real time applications such as
				vending machine and washing machines etc CO1: Design and analyse the various linear
				applications of op-amp
				CO2: Design and analyse the various non-linear
				applications of op-amp
				CO3: Design and analyse filter circuits using op-
FA				op-





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22	111-1	LC	Linear	amp
			Integrated	CO4: Design and analyse oscillators and multi
			Circuits and	vibrator circuits using op-amp
			Applications Lab	CO5: Design and analyse the various applications
			Lab	of 555 timer.
				CO6: Analyse the performance of oscillators and
				multivibrators using PSPICE
				CO1: Students will able to understand basic theories of Digital Communication system in
				practical.
				CO2: Can able to design and implement different
			Digital	modulation and demodulation techniques.
23	111-1	LC	Communication	CO3: Students Can measure the bandwidth of
			s Lab	various modulation techniques and observes the
				output waveform
				CO4: Emphasize on sampling modeling,
				techniques, signal constellations
-				CO1: Can able to perform channel coding.
				CO1: Students can develop Assembly Language
				Program by using MASM /TASM software. CO2: Design and implement programs on 8086
				micro processor.
				CO3: Design interfacing circuits and implement
			Microprocessor	corresponding programs on 8086 micro
24	111-1	LC	and	processor.
			Microcontroller	CO4: Design and implement 8051 microcontroller
			s Lab	based systems.
				CO5: Able to develop Assembly Language
				Program for ARM Cortex M3 Processor using KEIL
				MDK ARM.
				CO1: Design and analyze wire antennas, loop
				antennas, reflector antennas, lens antennas, horn
				antennas and micro strip antennas
			Wired And	CO2: Quantify the fields radiated by various types
25	111-11	PC	Wireless	of antennas
23			Transmission	CO4: Applying antenna arrays
			Devices	CO4: Analyze antenna measurements to assess antenna's performance
				artierina s performance





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				COS LL NIC NI AMARAVATI - AP - 521456
				CO5: Identify the characteristics of radio wave
				propagation COL Demonstrate
				CO1: Demonstrate a clear understanding of
				CMOS fabrication flow and technology scaling.
				CO2: Apply the design Rules and draw layout of
				a given logic circuit.
26	111-11	PC	Very Large Scale	CO3: Design basic building blocks in Analog IC
			Integrated	design.
			Circuits	CO4: Design MOSFET based logic circuits using
				various logic styles like static and dynamic CMOS
				CO5: Design FPGA Architectures and Analyze the
				behaviour of static and dynamic logic circuits.
				CO1: Formulate engineering problems in terms of DSP operations
				CO2: Analyze digital signals and systems
			Digital Signal	CO3: Analyze discrete time signals in frequency domain
27	111/11	PC	Processing	
			rrocessing	CO4: Design digital filters and implement with
				different structures
				CO5: Understand the key architectural
				CO1: Identify the limitations of conventional
				mobile telephone systems; understand the
				concepts of cellular systems.
				CO2: Understand the different types of
			Cellular And	interferences and design of antenna system
28	111-11	PE	Mobile	CO3: Understand the frequency management,
			Communication	channel assignment strategies and antennas
			S	CO4: Understand the concepts of handoff and
				architectures of various cellular systems.
				CO5: Understand the concepts of GSM, GPRS
				LTE, Wi-MAX and 3G,4G and 5G
				CO1: Explain the characteristics of various power
				semiconductor devices and understand the gate driver circuits.
29		05	Power	CO2: Explain the operation of single-phase full
29	111-11	OE	Electronics	wave converters and perform harmonic analysis.
				CO3: Explain the operation of three phase full—
No. of the Lot of the				wave converters and perform harmonic analysis.
EGEO	2			CO4: Analyze the operation of different types of

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	T	T		DC DC
				DC- DC converters.
				CO5: Explain the operation of inverters and
				application of PWM techniques for voltage
				control and harmonic mitigation.
-				
				CO1: Understand about Design Principles, Business
				Process, Architecture of IOT system and its role in
				Cloud.
				CO2: Understand about Mini Computers usage
				to develop IOT Services and Embedded
20				Processor(Cortex- M0).
30	111-11	PC	INTERNET OF	CO3: Understand about various IOT Protocols
			THINGS	for Communication Purpose, Bluetooth Low
				Energy Architecture.
				CO4: Understand about Solution Framework for
				IOT Applications.
				CO5: Able to Design Real Time IOT based
				applications.
				CO1: Demonstrate a clear understanding in
				hardware design language Verilog HDL.
				CO2: Able to model a combinational circuit using
				hardware description language Verilog HDL and
				validate its functionality.
31	111-11	LC	VLSI Lab	CO3: Able to model a Sequential circuit using
			V EST EUD	hardware description language Verilog HDL and
				validate its functionality.
				CO4: Able to implement 8-bit synchronous up
				down counter on FPGA.
				CO5: Able to implement 4-bit sequence detector
				through Mealy and Moore state machines.
				CO1: Students Can Verify Linear and Circular
				Convolution for two Discrete Time Signals by
				using MATLAB.
				CO2: Able to design FIR Filter (LP/HP) using
				windowing technique.
32	111-11	LC	Digital Signal	CO3: Able to implement IIR Filter(LP/HP) on DSP
			Processing Lab	Processors.
				CO4: Able to implement N-point DFT Algorithm.
			0,000	CO5: Able to implement FFT Algorithm.
		61.60	113	9





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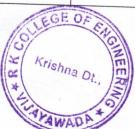
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CO1: Derive the radar range equation and to solve some analytical problems. CO2: Understand the different types of radars and its applications. CO3: Understand the concept of tracking and different tracking techniques. CO4: Understand the concept of tracking and different tracking techniques. CO5: Understand the various components of radar receiver and its performance. CO1: Perform image manipulations and different digital image processing technique CO2: Perform basic operations like – Enhancement, segmentation, compression CO3: Analyze pseudo and fullcolor image processing techniques. CO4: Apply various morphological operators on images. CO5: Perform Image transforms and restoration techniques on image. CO1: understand osi and tcp/ip models CO2: analyze mac layer protocols and lan technologies CO3: design applications using internet protocols CO4: understand routing and congestion control algorithms CO5: understand how internet works CO6: Choose necessary components required in modern optical communications systems
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1 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3
CO2: Design and build optical fiber experiments
In the laboratory, and learn how to calculate
electromagnetic modes in waveguides, the amount of light lost going through an optical
system, dispersion of optical fibers
Communication CO3: Understand and Analyze various analog
continuous wave modulation and demodulation techniques including AM, FM and PM
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optical test equipment to analyze optical fiber a light wave systems CO5: Choose the optical cables for being the communication and	
CO5: Choose the optical cables for be	
CO5: Choose the optical cables for be	
communication to be	+00
Collinunication with minimum losses Desi	rer
communication with minimum losses Desi build, and demonstrate optical fiber experime	gn,
in the laboratory	nts
CO1: Evaluate the time and space parameters of switched signal	f a
CO2: Establish the digital signal path in time a	nd
37 IV-1 Electronic space, between two terminals	
Switching CO3: Evaluate the inherent facilities within to	he
Systems system to test some of the SLIC, CODEC a	nd
digital switch functions.	
CO5. Investigate the traffic capacity of the system	n.
CO3: Evaluate methods of collecting traffic data	
COI: Understand the basic concepts of	an
embedded system and able to know	an
embedded system design approach to perform	211
specific function.	a
CO2: The hardware components required for a	
embedded system and the design approach of a	an
38 IV-I Embedded embedded hardware.	an
CO2. The	
System System approaches on embedded firmware designation approaches on embedded environment.	gn
CO4: Understand how to intermed.	
CO4: Understand how to integrate hardware an	d
firmware of an embedded system using real time	e
CO5: The werieve I I I I	
CO5: The various embedded firmware desig	n
approaches on embedded environment.	
CO3: Apply the characteristics of Reflex Klystron.	
CO2: Analyze various parameters of Waveguid	e
Components.	
CO3: Estimate the power measurements of RI	=
Micro Wave Components such as directional Couplers	
59 IV-I Engineering & CO4: Demonstrate characteristics of various	5
Optical Lab Optical sources.	
CO5: Measure data Rate, Numerical Aperture and	
Losses in Optical Link.	
CO1: Students Can Verify Linear and Circular	+
Convolution for two Discrete Time Signals by	

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40	IV-I		Digital Signal	using MATLAB.
			Processing Lab	Signature of the state of the s
				windowing technique.
				CO3: Able to implement IIR Filter(LP/HP) on DSF
				Processors.
				CO4: Able to implement N-point DFT Algorithm.
		-		CO5: Able to implement FFT Algorithm.
				CO1: Identify the limitations of conventional
				mobile telephone systems; understand the
				concepts of cellular systems.
				CO2: Understand the different types of
41			Cellular And	interferences and design of antenna system
41	IV-11		Mobile	CO3: Understand the frequency management
			Communication	channel assignment strategies and antennas
			S	CO4: Understand the concepts of handoff and
				architectures of various cellular systems.
				CO5: Understand the concepts of GSM, GPRS
				LTE, Wi -MAX and 3G,4G and 5G
				CO1: Select the instrument to be used based on
				the requirements
				CO2: Understand and analyze different signal
				generators and analyzers.
			Electronic	CO3: Understand the different types of
42	IV-II		Measurement	Oscilloscopes for different applications
			and	CO4: Understand and analyze the concepts
			Instrumentation	Bridge circuits in measuring equipment
			- Internation	CO5: Design different transducers for
				measurement of different parameters
				CO6: Understand the Measurement of physical
				parameters like force, pressure etc
				CO1: Understand the concepts applications of
				sateritie communication
				CO2: Derive the expression for G/T ratio and to
43	11/11			solve some analytical problems on satellite link
45	IV-11		Satellite	design.
			Communication	CO3: Derive the expression for G/T ratio and to
				solve some analytical problems on satellite link
		***		design.
	(EG	96		CO4: Understand the various types of multiple
	1121	101		The state of maniple



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4		(M), Vijayawada, AMARAVATI - AP - 521456		
				access techniques and architecture of earth station design.
				CO5: Understand the various types of multiple
				access techniques and architecture of earth station
		-		design.
				CO6: Understand the concepts of GPS and its
				architecture.
	IV-II		Operating Systems	CO1: Design various Scheduling algorithms
44				CO2: Apply the principles of concurrency
				CO3: Design deadlock, prevention and avoidance
				algorithms
				CO4: Compare and contrast various memory
				management schemes.
				CO5: Design and Implement a prototype file systems.
45	IV-II		project	CO1: Analyze new problems, identify and define the appropriate requirements for its solutions.
				CO2: Understand of the impact of engineering solutions.
				CO3: Understand team work to complete a common goal

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